

EECS 388: Computer Systems and Assembly Language Homework 6

Due: Thursday May 8

1. (10 points) Specify the contents of the ATDCTL5 register to configure the ATD system for a continuous sequence of conversions with four conversions per sequence on ATD channels 0 through 3. Where are the results of these conversions placed?
2. (30 points) Page 391, Advanced #6.
3. (30 points) Page 391, Challenging #1.
4. (30 points) Page 391, Challenging #2

5. (10 points) Page 436, Fundamental #1
6. (10 points) Page 436, Fundamental #2
7. (10 points) Page 436, Fundamental #3
8. (10 points) Page 436, Fundamental #4
9. (10 points) Page 436, Fundamental #5