Homework #5
EECS 388
Due: December 5, 2007

Problem #1.
List the internal operations that the Microblaze performs when it initially responds to an external interrupt. (You can use pseudo code to outline the operations)

Problem #2
List the internal registers of the interrupt controller and describe their operations and usage.

Problem #3?
Suppose you have two interrupting devices, dev1 and dev2 hooked up into interrupt level 0 and 1 respectively of our interrupt controller. Write an assembly routine that checks to see if one or both of the devices have a pending interrupt request in the interrupt controller. You don't have to worry about "handshaking" either device.

Problem #4.
Write a routine that sets the interrupt controller to output a clock at 150 Mhz with a duty cycle of 40%. Your routine should first set up the counters and then initialize execution of the output clock.